

Microelectronics Packaging Issues of Microelectromechanical Systems (MEMS)

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Microelectromechanical systems (MEMS) is a very important field of research and development that has shown a significant promise in the last decade for a variety of applications such as air-bag, pressure sensors, etc. Standard semiconductor microelectronics packaging needs the integrated circuits to be protected from the harsh environment, and provide electrical communication with the other parts of the circuit, facilitate thermal dissipation efficiently, and impart mechanical strength to the silicon die. Microelectronics packaging involves wafer dicing, bonding, lead attachment, encapsulation to protect from the environment, electrical integrity, and package leak tests. In the case of MEMS the microstructures often interfaces with the hostile environment where packaging leak tests and testing of such devices using chemical and mechanical parameters will be very difficult and expensive. Packaging of MEMS is significantly complex as they serve to protect from the environment and microstructures interact with the same environment to measure or affect the desired physical or chemical parameters. The most of the silicon circuitry is sensitive to temperature, moisture, magnetic field, light, and electromagnetic interference. The package must then protect the on-board silicon circuitry while simultaneously exposing the microsensor to the effect it measures. MEMS technology has a major application in developing a microspacecraft for space systems. This technology would eventually miniaturize many of the components of the spacecraft to reach the NASA's goal of faster, cheaper, better, smaller spacecraft to explore the space. This paper discusses the issues in the packaging of MEMS based on silicon, poly-silicon, and other materials for microsapcecraft applications considering the space environment and qualification procedures.